

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L12	2	"5903600".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L13	18	"693835"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 14:09
L14	768	(pll with receiver) with (pll with transmitter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L15	4277	375/376	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L16	100	L14 and L15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L17	2	"5903600".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L18	2	"5790606".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 14:09
L19	25	(pll with receiver) with (pll with transmitter) with different	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L20	3	"9626578"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 14:09

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L21	2	"5903600".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L22	16	("3921095" "4061979" "4238740" "4365210" "4389622" "4419633" "4625180" "4667169" "4812783" "4841255" "4951005" "5008629" "5103192" "5124669" "5128632" "5304951").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/19 14:09
L23	104	(pll with receiver) and (pll with transmitter) and (switch with transceiver)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:11
L24	1771	375/373	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L25	32	L14 and L24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L26	1939	455/260	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L27	72	L14 and L26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L28	178	375/215	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L29	4	L14 and L28	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09

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L30	18	(pll with receiver) and (pll with transmitter) and ((simplex) or (semi with duplex))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:14
L31	3743	375/354	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L32	28	L14 and L31	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L33	11	"08/907,594"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L34	18	(pll with receiver) and (pll with transmitter) and (simplex)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L35	1	((pll with receiver) and (pll with transmitter) and ((simplex) or transmission with only))).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L36	0	(pll with receiver) with (pll with transmitter) with diffrent	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L37	234	455/180.3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L38	3175	327/156	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L39	1320	327/147	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09

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L40	25	L14 and L39	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L41	1120	375/327	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:28
L42	213	375/294	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L43	979	(pll with receiver) and (pll with transmitter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L44	0	(pll with receiver) with (pll with transmitter) with diffrent	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L45	1	((pll with receiver) and (pll with transmitter) and (simplex or (transmission with only) or (reception with only))).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L46	0	(pll with receiver) and (pll with transmitter) and (switch with tranceiver)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L47	1	((pll with receiver) and (pll with transmitter) and (simplex or (transmission with only))).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L48	0	(pll with receiver) and (pll with transmitter) and (semi with duplex)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L49	6	L14 and L42	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09

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L50	0	(pll with receiver) and (pll with transmitter) and semi adj duplex	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L51	61	L14 and L38	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L52	38	L14 and L41	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L53	13	L14 and L37	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L54	0	((pll with receiver) and (pll with transmitter) and (simplex)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L55	25	(pll with receiver) with (pll with transmitter) with different	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:09
L56	0	pll with (mininum adj time) with before	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:12
L57	1	pll with (minimum adj time) with before	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:14
L58	979	(pll with receiver) and (pll with transmitter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:14
L59	1	pll same (minimum adj time) with before	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:14

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L60	29	pll and (minimum adj time) with before	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:15
L61	1	58 and 60	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:14
L62	2	(pll and (minimum adj time) with before).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:15
L63	2	"6535968".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:29
L64	2	"4943787".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/19 14:29

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Application
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IDS Flag Clearance for Application 09966507

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Content	Mailroom Date	Entry Number	IDS Review	Last Modified	Reviewer
M844	2005-06-22	17	Y <input checked="" type="checkbox"/>	2005-10-06 12:04:42.0	jtorres1
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Continuity Information for 09/966507

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Foreign Information for 09/966507

Priority#	Date	Country
2000-61478	10/19/2000	REPUBLIC OF KOREA

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Date: 10/19/2006
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Inventor Information for 09/966507

Inventor Name	City	State/Country
JO, KANG-HYUN	SEOUL	KOREA, REPUBLIC OF
LEE, KYUNG-MIN	SEOUL	KOREA, REPUBLIC OF

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**Inventor Name Search Result**

Your Search was:

Last Name = JO

First Name = KANG-HYUN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09966507	Not Issued	80	09/28/2001	Apparatus and method for generating transmission and reception local oscillation signals in a mobile terminal	JO, KANG-HYUN

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = LEE

First Name = KYUNG-MIN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08777761	5903600	150	12/30/1996	AUTOMATIC FREQUENCY CONTROL CIRCUIT FOR DIGITAL RADIO COMMUNICATION SYSTEM	LEE, KYUNG-MIN
09737372	6546234	150	12/15/2000	APPARATUS AND METHOD FOR COMPENSATING RECEIVED SIGNAL STRENGTH INDICATOR ACCORDING TO TEMPERATURE	LEE, KYUNG-MIN
09966507	Not Issued	80	09/28/2001	Apparatus and method for generating transmission and reception local oscillation signals in a mobile terminal	LEE, KYUNG-MIN
10177723	6788254	150	06/21/2002	WIDEBAND INTERNAL ANTENNA WITH ZIGZAG-SHAPED CONDUCTIVE LINE	LEE, KYUNG-MIN
10896964	Not Issued	20	07/23/2004	Computer system	LEE, KYUNG-MIN
11349169	Not Issued	25	02/08/2006	Method for reconfiguring application using subtyping-based flexible service adaptation in pervasive computing environment and system thereof	LEE, KYUNG-MIN

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name
LEE	KYUNG-MIN

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Correspondence Address for 09/966507

Customer Number	Contact Information	Address
28249	Telephone: (516)228-8484 Fax: (516)228-8516 E-Mail: No E-Mail Address	DILWORTH & BARRESE, LLP 333 EARLE OVINGTON BLVD. UNIONDALE NY 11553

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The lock time is the **minimum time** required for **PLL** output stabilization. ... instead of the UpI(PLL output) **before** the S/W newly configures the UPLLCON. ...

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Before PWROK. assertion, the AMD Athlon processor is clocked by a ring. oscillator. This **minimum time** is not specified. The AMD Athlon processor **PLL** is ...

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perform the required maintenance on the equipment **before** a. scrap incident occurs. Case Study 4: **PLL** Pressure Reading Error ...

ieeexplore.ieee.org/iel3/4868/13441/00616326.pdf?arnumber=616326 - [Similar pages](#)

[PDF] On-chip calibration technique for delay line based BIST jitter ...

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is enabled by the calibration control logic, selecting the **PLL** under ... a LPF with a **minimum time** constant of 0.65 μ s. This will guarantee ...

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The Jitters

The basic premise of a **PLL** is that it carefully adjusts its own clock, ... carefully about the consequences **before** you move ahead with your system design. ...

www.sigcon.com/Pubs/straight/jitters.htm - 18k - [Cached](#) - [Similar pages](#)

iSYSTEM Rolls Out winIDEA 9.4 with New CPU and Device Support

Added optional delay for **PLL** initialization **before** emulator starts using second clock ... 50-bit time stamp counter (no time limit); 20ns **minimum time** stamp ...

www.embeddedstar.com/press/content/2004/5/embedded14403.html - 25k -

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Silicon Devices Circuit Resources: VCO Modelling and Simulation ...

The transient simulation's **minimum time**-step should be set fine enough to prevent magnitude ... in FMmod.zip) to emulate the behaviour of the VCO in a **PLL**. ...

www.silicondevices.com/Resources/AppsNotes/ModellingVCOs.html - 21k -

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EETimes.com

However, the **PLL**, which provided a critical function for the chip, contained analog and ...

Co-verification allows maximum flexibility and **minimum time** to ...

www.eetimes.com/editorial/1997/coverstory9707.html - 70k - [Cached](#) - [Similar pages](#)

[PDF] AV9110 Serially Programmable Frequency GeneratorFile Format: PDF/Adobe Acrobat - [View as HTML](#)

CE is taken high **before** 24 clock cycles have elapsed, the data ... The AV9110 is a classical **PLL** circuit and the VCO output frequency is given by: ...

www-ese.fnal.gov/eseproj/SVX/BERT/PLL_clock_chip.pdf - [Similar pages](#)

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PLL response with modulation. Figure 8. The FFT of the data from Fig 8. ... The arm delay sets the **minimum time** from an arm event to the first measurement ...

www.wavecrest.com/technical/pdf/GigaView_Getting_Started_Guides/GHFM.PDF -

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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

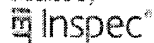
IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. A variable delay line PLL for CPU-coprocessor synchronization
Johnson, M.G.; Hudson, E.L.;
[Solid-State Circuits, IEEE Journal of](#)
Volume 23, Issue 5, Oct. 1988 Page(s):1218 - 1223
Digital Object Identifier 10.1109/4.5947
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